

Amendments to the Specification:

Please delete the Abstract of the invention in its entirety, and add the following new

Abstract:

A mirror suppression circuit includes a first quadrature signal path coupled between quadrature signal input and output terminals and an error correction circuit for correction of amplitude and phase errors in a carrier modulated quadrature signal. To obtain a suppression of both amplitude and phase imbalance of the carrier modulated quadrature signal as well as signal amplitude variations, a quadrature output of the error correction circuit is coupled to an error detection circuit. The error detection circuit detects amplitude and phase errors and provides amplitude and phase control signals to the error correction circuit for a negative feed back of the amplitude and phase errors. The amplitude and phase control signals vary with products of components of the quadrature signal and components of a quadrature reference signal.